3V-40V Vin, 300mA, 2.4uA I_Q, Low-Dropout Regulator with PG Feature

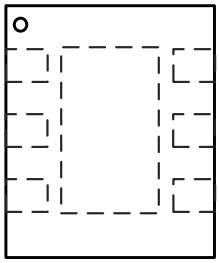
±

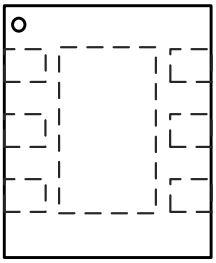
±

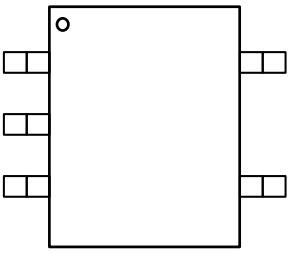
1.0: Release to production.

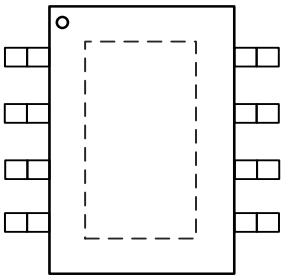
Revision 1.1: Update Figure 7.

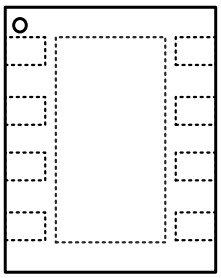
Revision 1.2: Update Part Number.







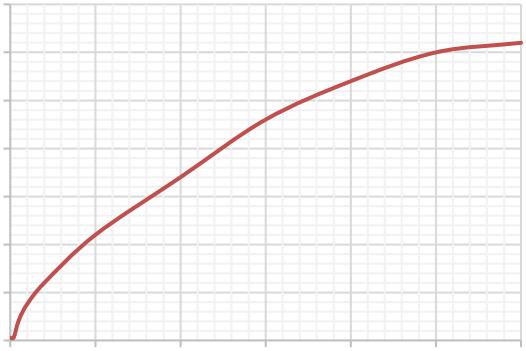


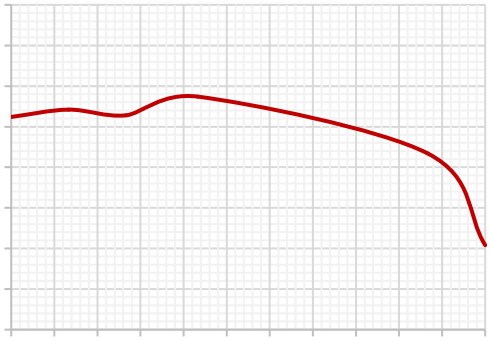


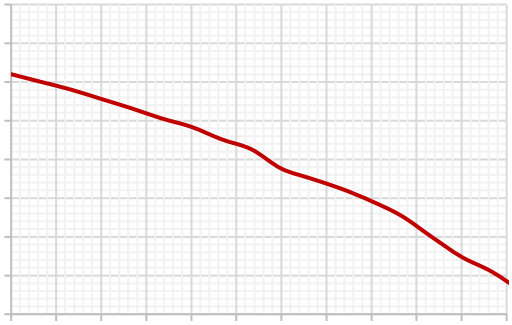
(1)

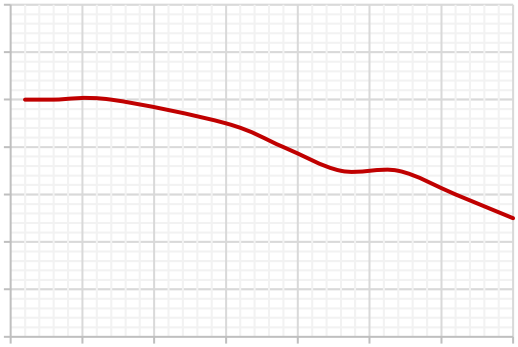
(2)

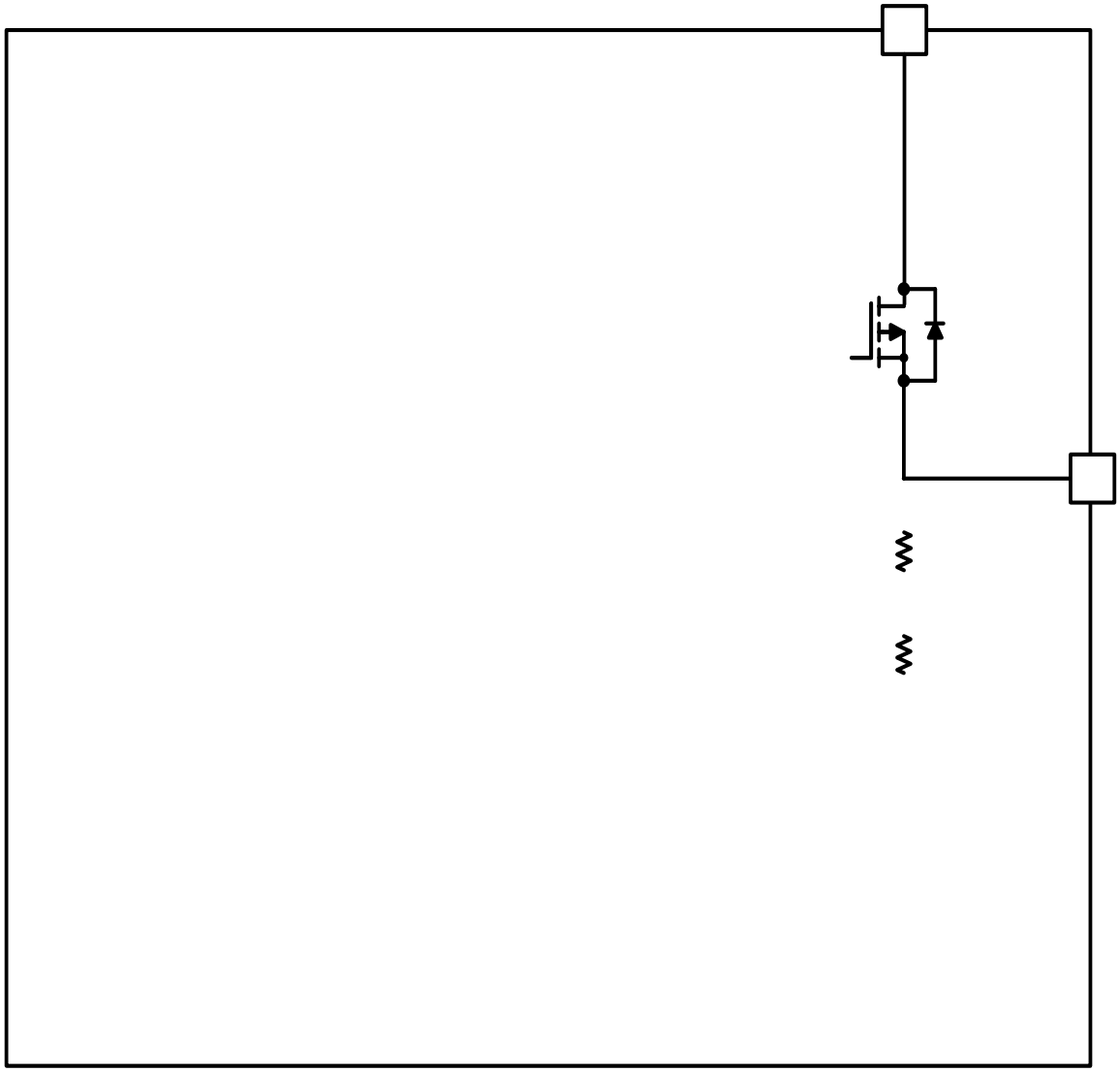
(3)

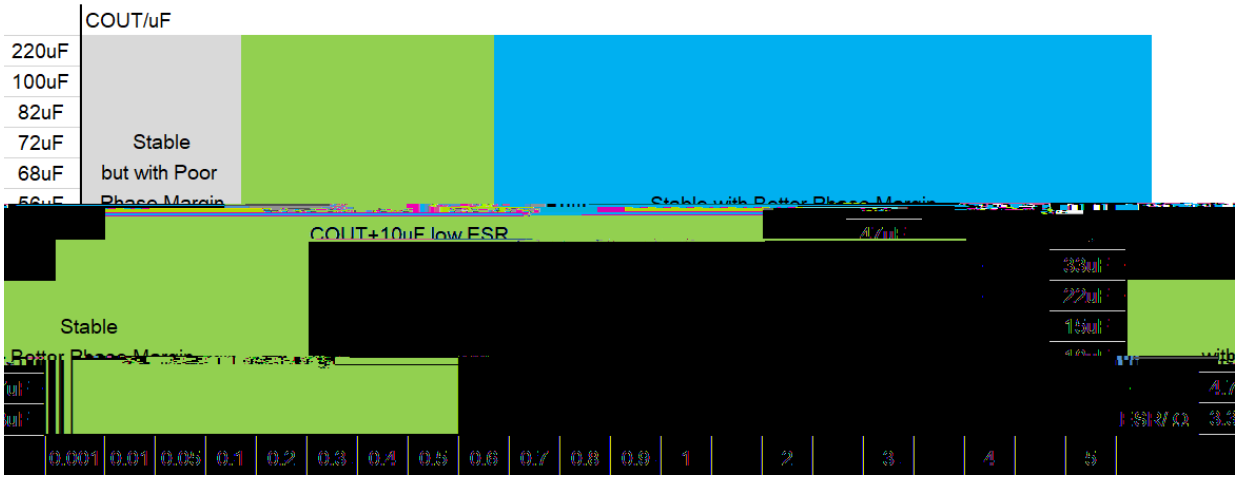


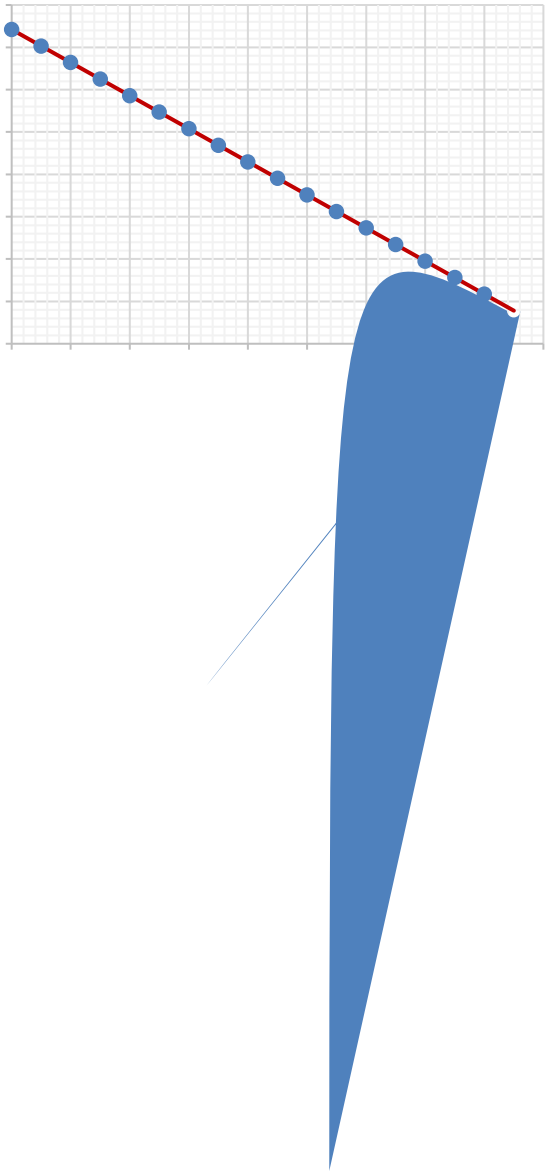


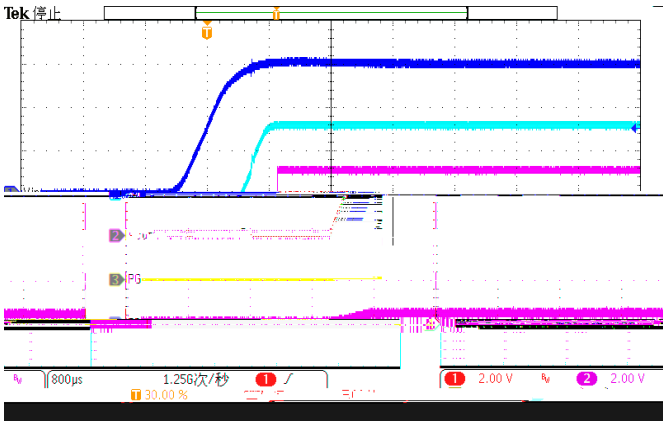


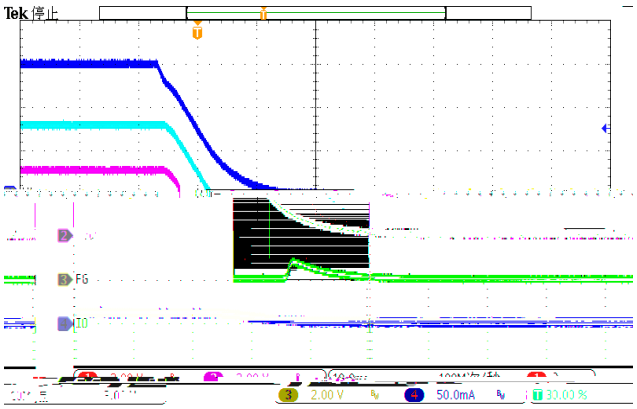


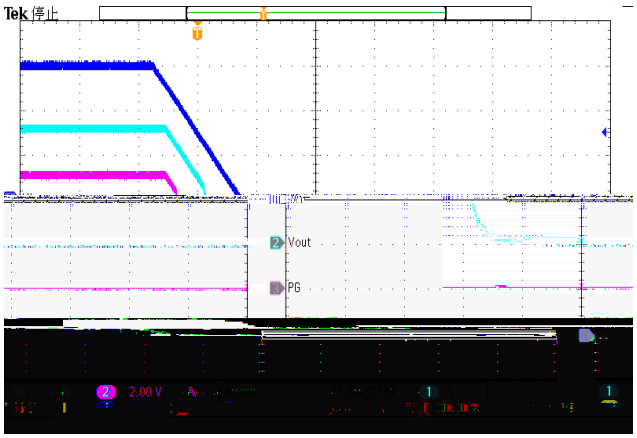


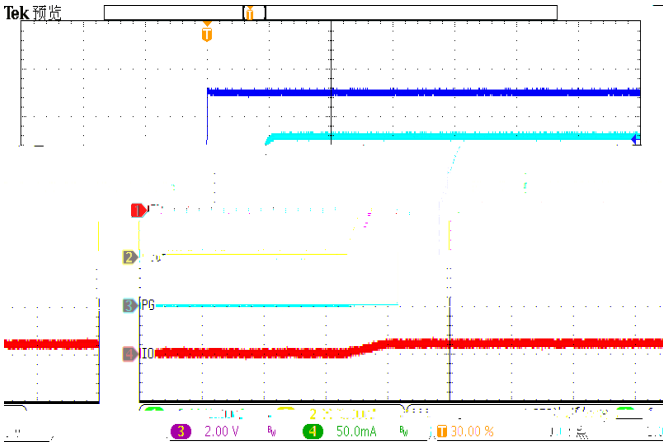

COUT/uF
220uF
100uF
82uF
72uF
68uF
Stable
but with Poor
Stable
0.001
0.01
0.05
0.1
0.2
0.3
0.4
0.5
0.6
0.7
0.8
0.9
1
2
3
4
5

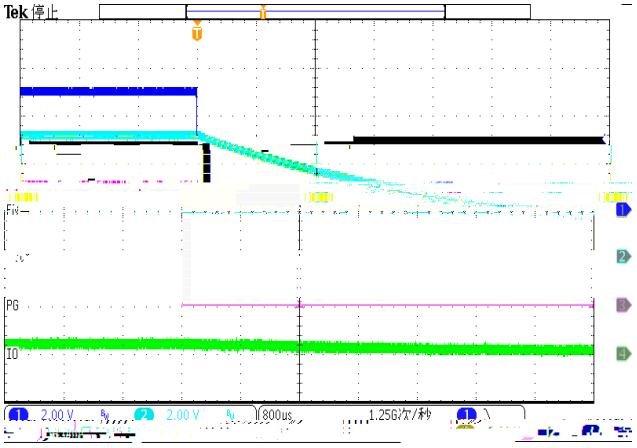


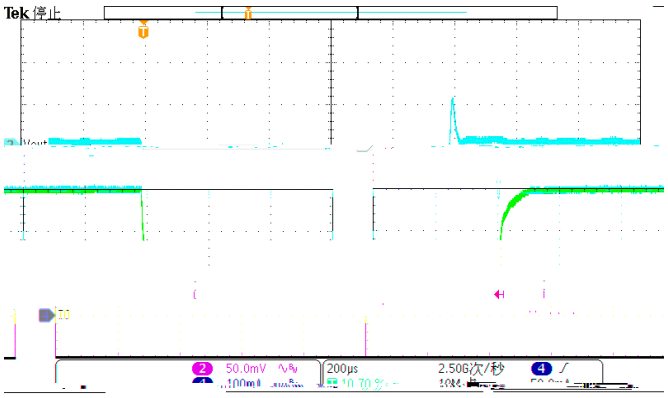

Tek 停止
PG
800µs
1.25G次/秒
2.00 V
2.00 V
30.00 %

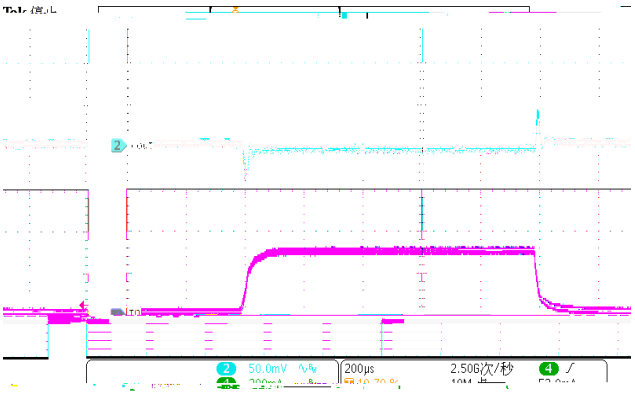

Tek 停止
FG
IO
2.00 V
50.0mA
30.00 %

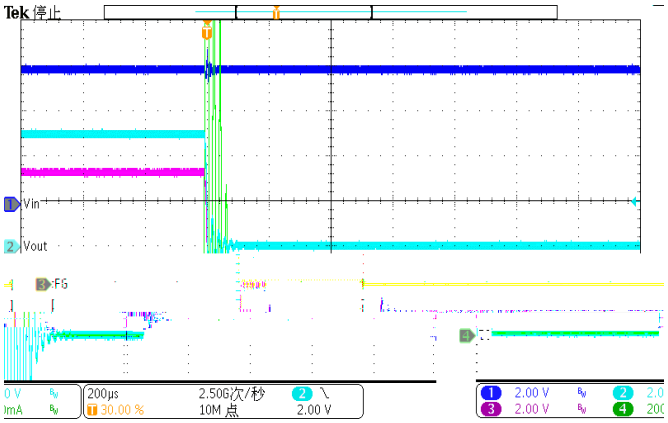

Tek 停止
Vout
PG
2.00 V

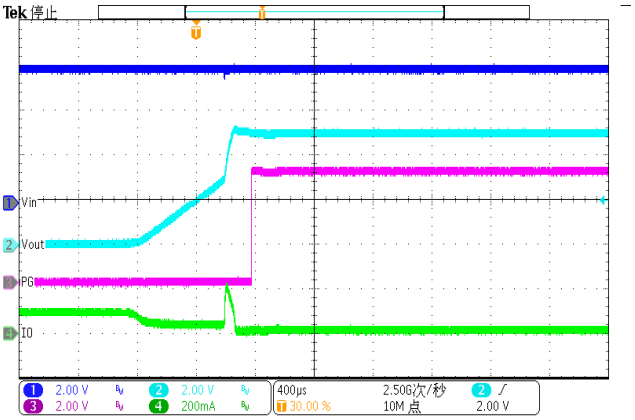

Tek 预览
PG
IO
2.00 V
50.0mA
30.00 %

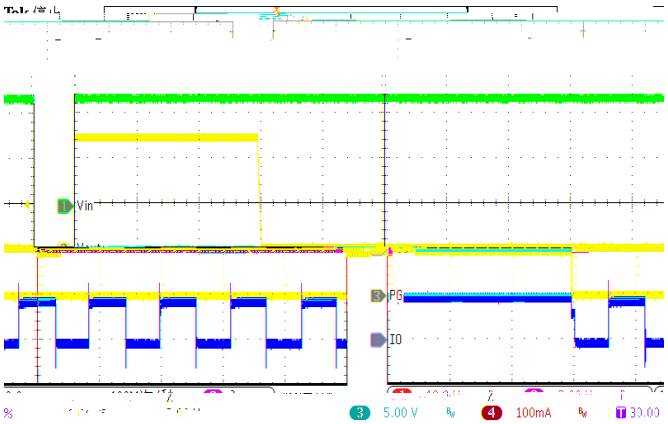

Tek 停止
PG
IO
2.00 V
2.00 V
800us
1.25G次/秒

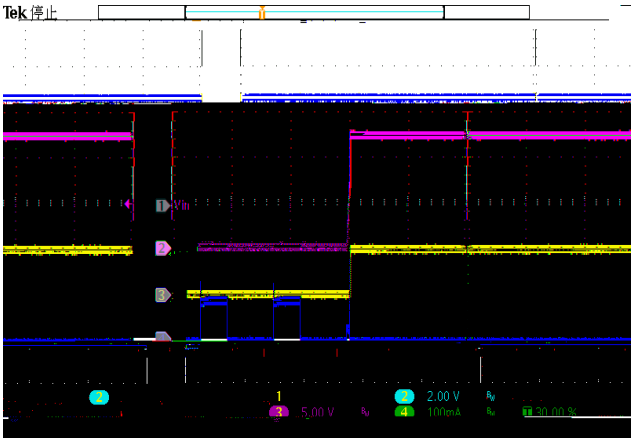

Tek 停止
Vout
IO
50.0mV
200µs
2.50G次/秒


Vout
Iout
50.0mV
200µs
2.50G次/秒


Tek 停止
Vin
Vout
FG
200µs
30.00 %
2.50G次/秒
10M 点
2.00 V
2.00 V
2.00 V


Tek 停止
Vin
Vout
PG
IO
2.00 V
2.00 V
2.00 V
200mA
400µs
30.00 %
2.50G次/秒
10M 点
2.00 V

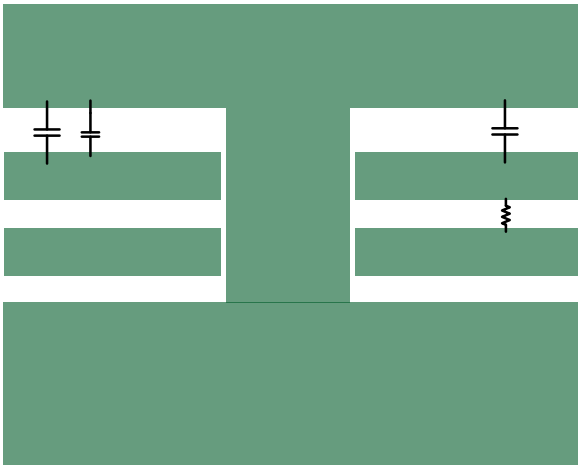

Vin
PG
IO
5.00 V
100mA
30.00

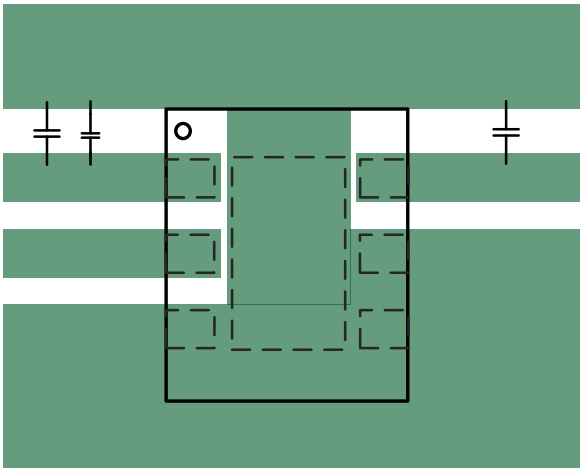

Tek 停止
Vin
2.00 V
5.00 V
100mA
30.00 %

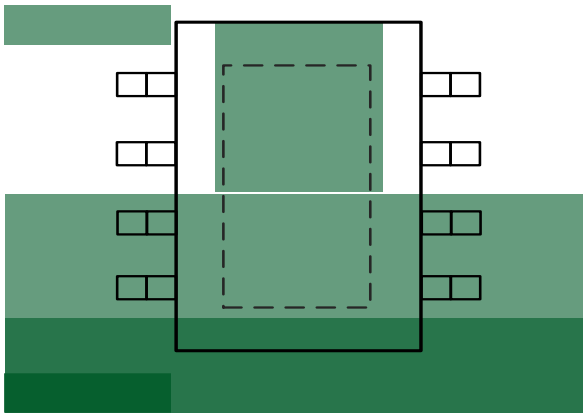
1.
2.

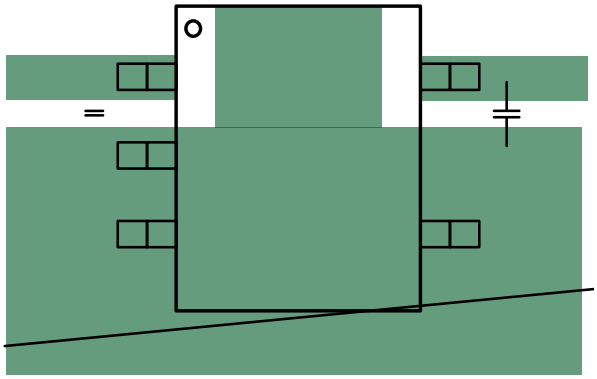
3.
4.

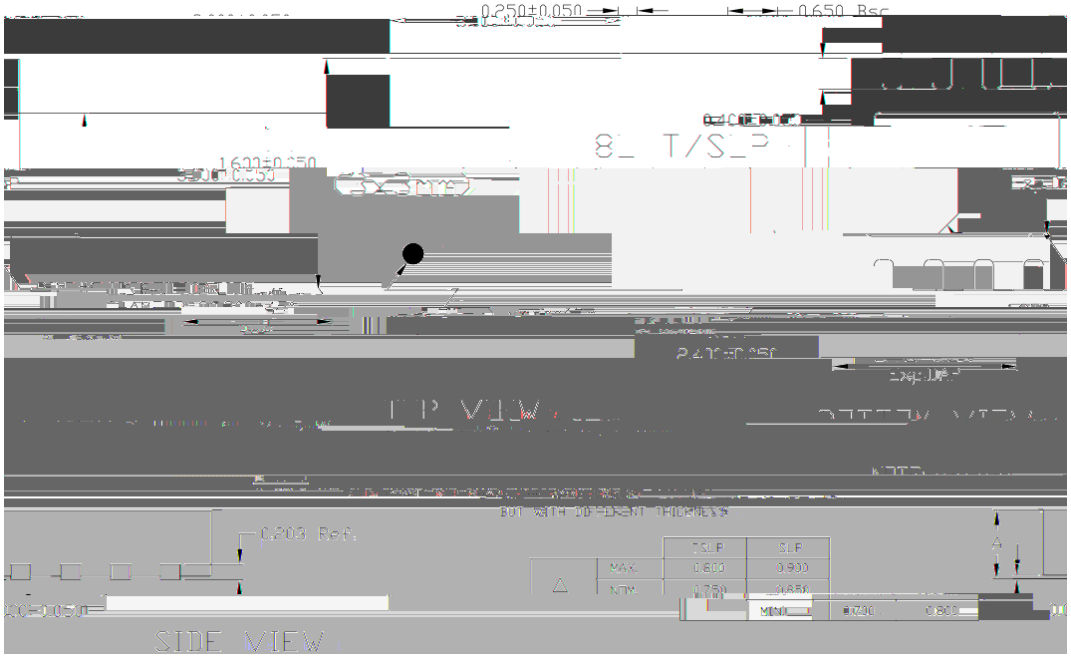
5.



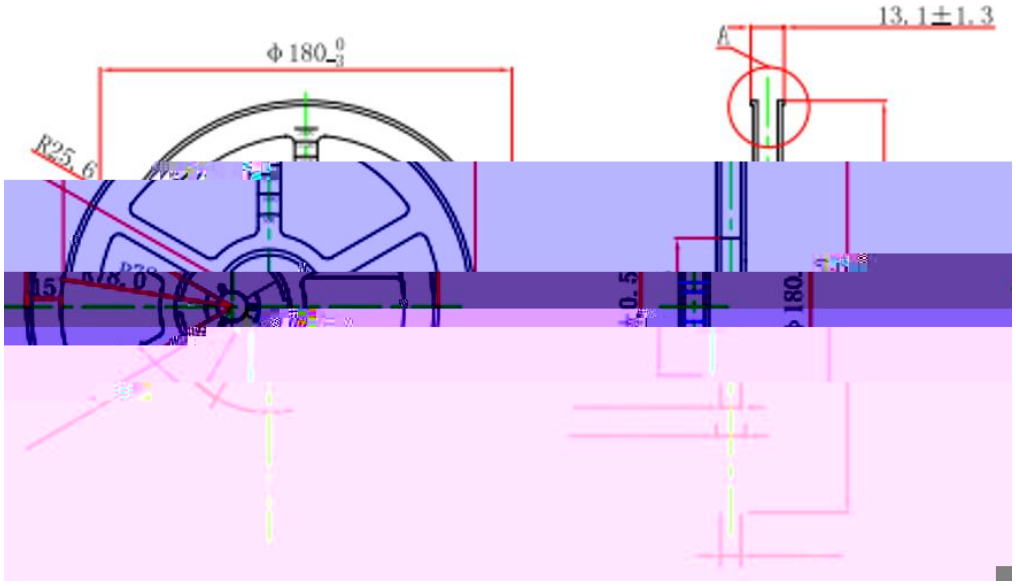


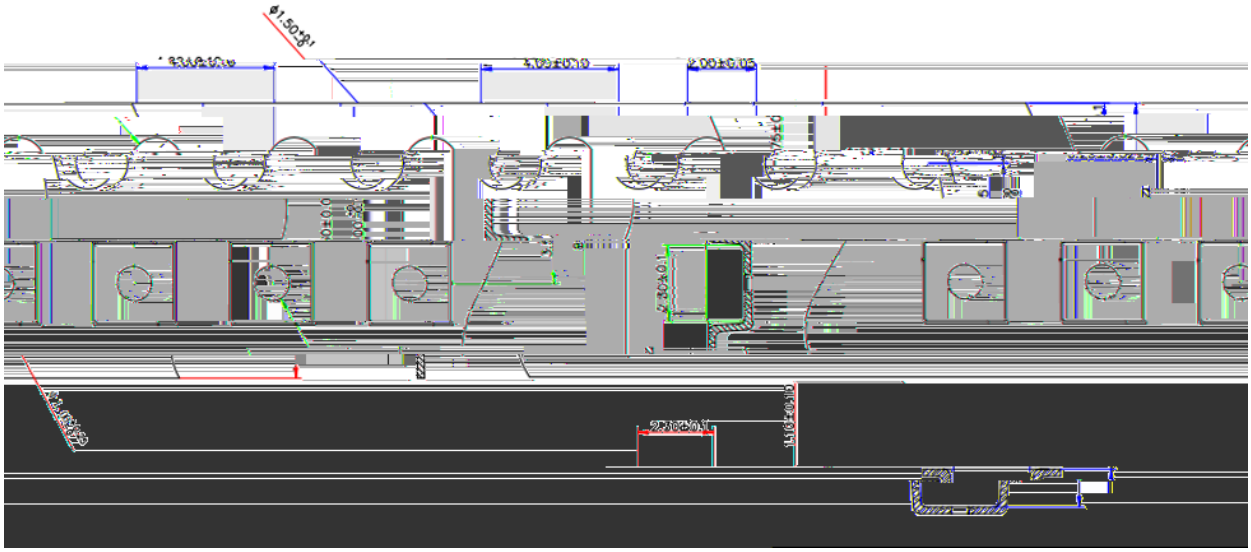




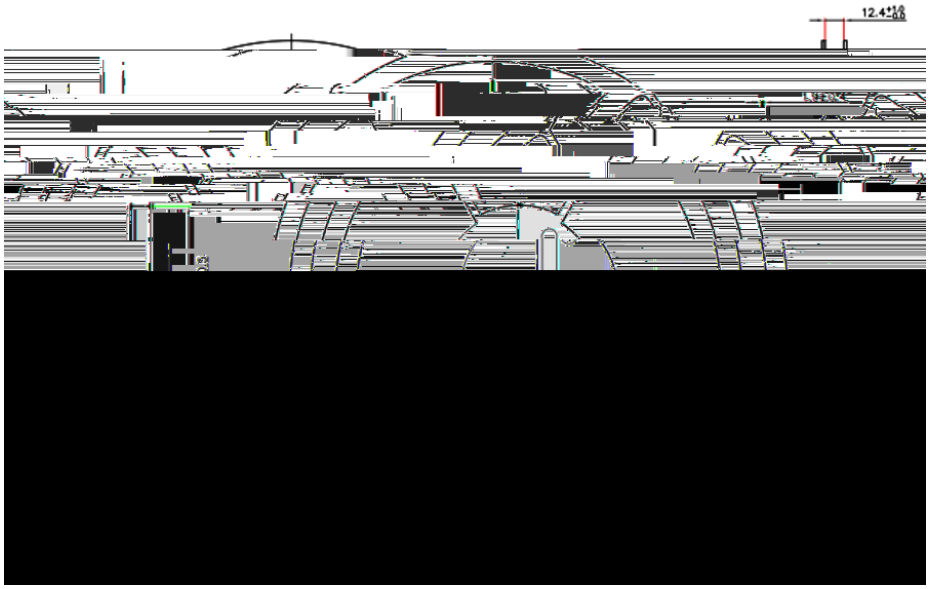

0.250±0.050
0.650 Bsc
TOP VIEW
BUT WITH DIFFERENT THICKNESS
0.203 Ref.
TSLP
SLP
MAX.
0.800
0.900
NOM.
0.750
0.850
MIN.
0.700
0.800
A
SIDE VIEW

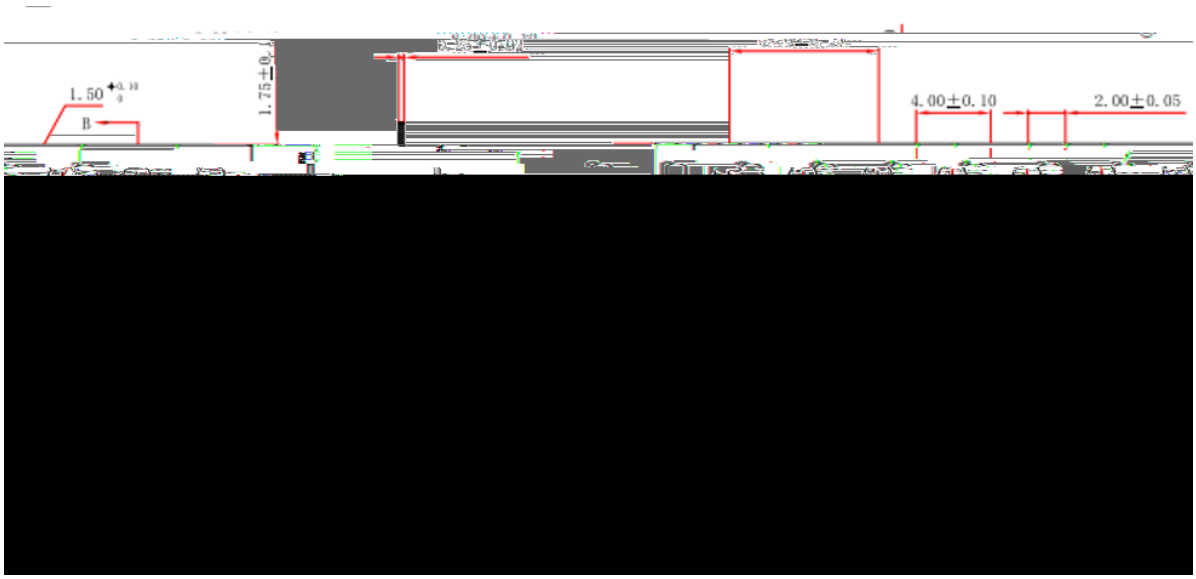
1.
2.
3.
4.
5.
6.


13.1±1.3
A
Φ180
R25.6
R20.0
15
0.5
Φ180



PIN1


12.4


1.50
B
1.75±0.
4.00±0.10
2.00±0.05

PIN1