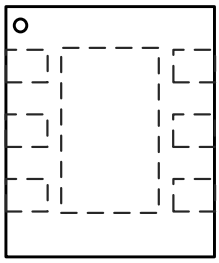
3V-40V Vin, 300mA, 2.4uA I_Q, Low-Dropout Regulator with PG Feature

Orderable Device	Output Voltage	Package	Package Marking	PINS	MSL



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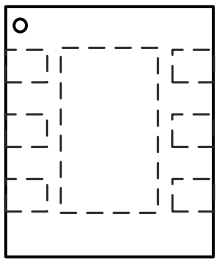

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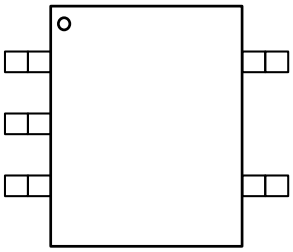

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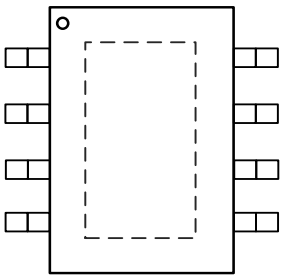

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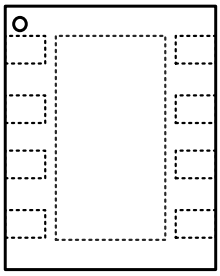

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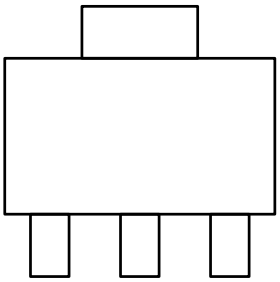


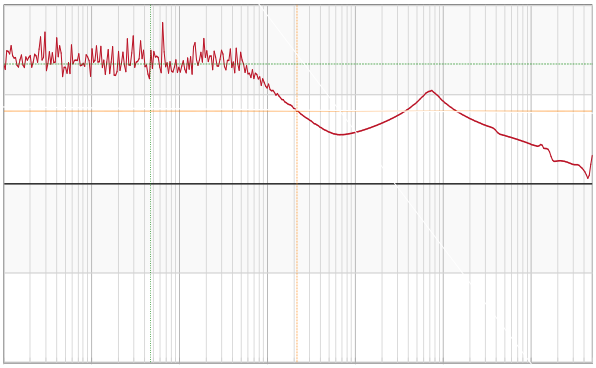
SCT71403FxxQTXER

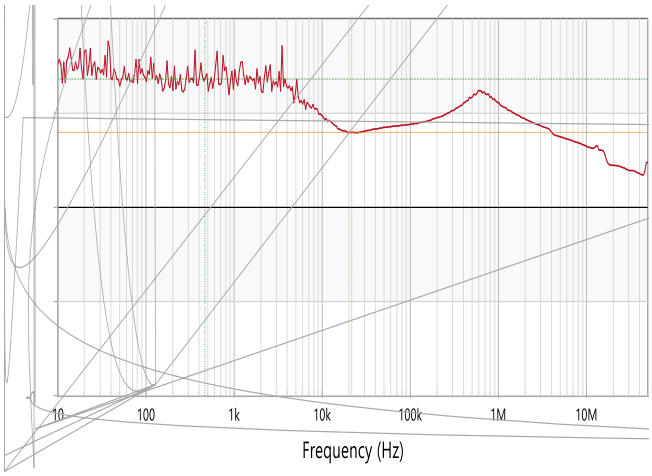
NAME	PIN NUMBER					PIN FUNCTION
	TQFN2X2-6L-A	TQFN2X2-6L	EMSOP-8L	SOT23-5L	TQFN3X3	

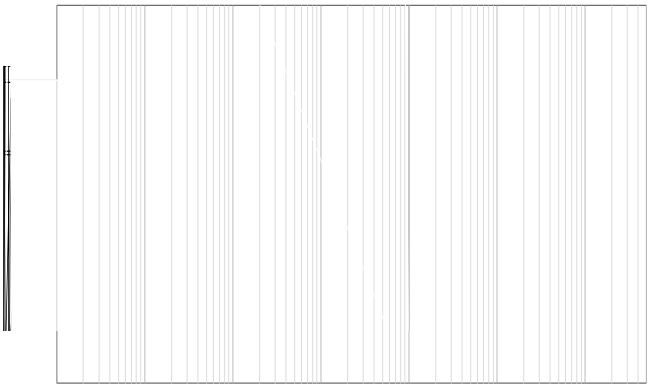
PARAMETER	DEFINITION	MIN	MAX	UNIT

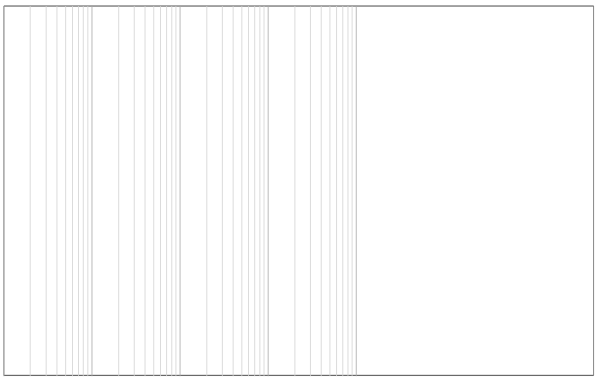
PARAMETER	DEFINITION	MIN	MAX	UNIT

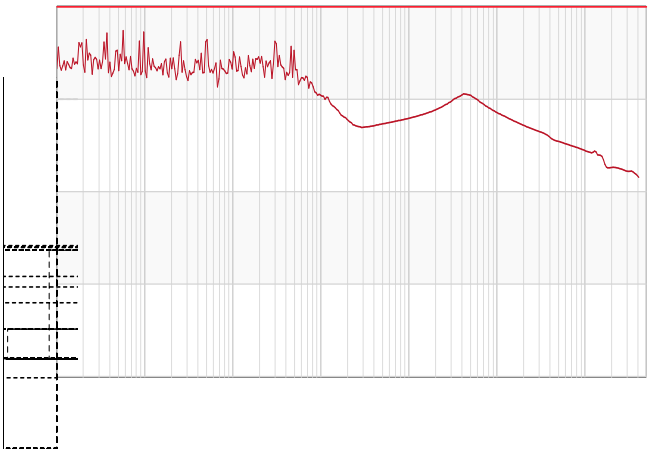
SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT

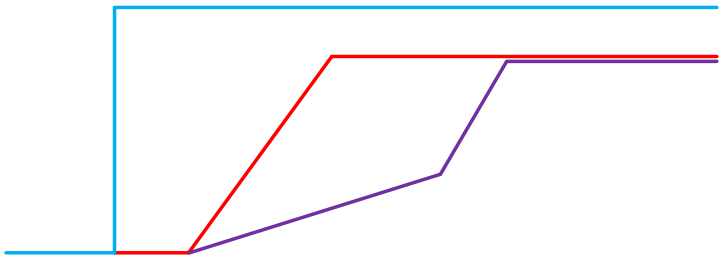



10
100
1k
10k
100k
1M
10M
Frequency (Hz)







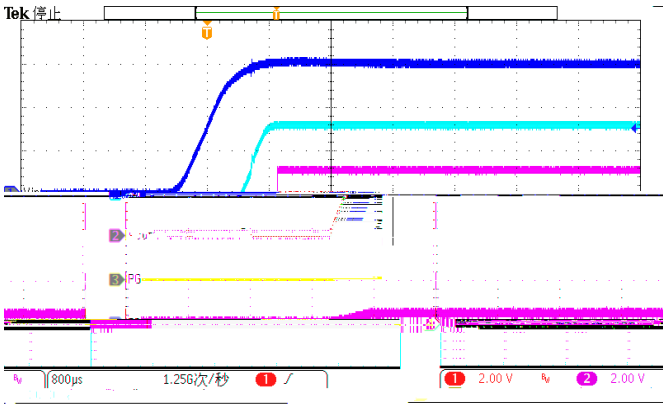


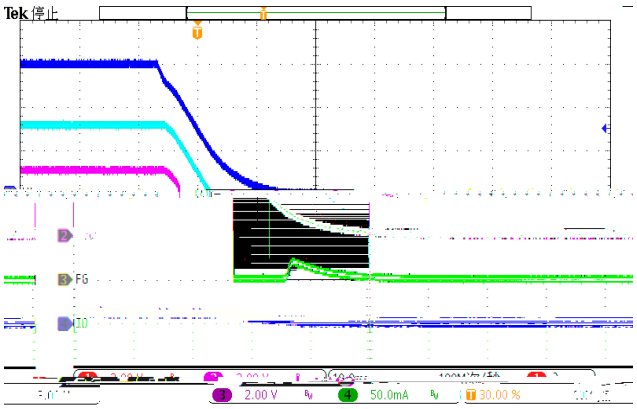
Power-Good and Power-Good Delay

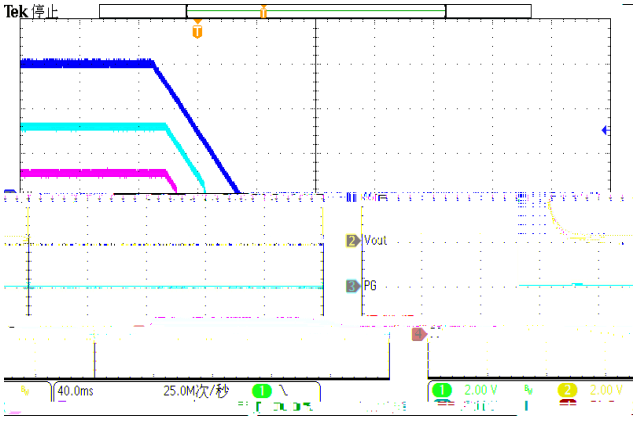
Input Capacitor and Output Capacitor

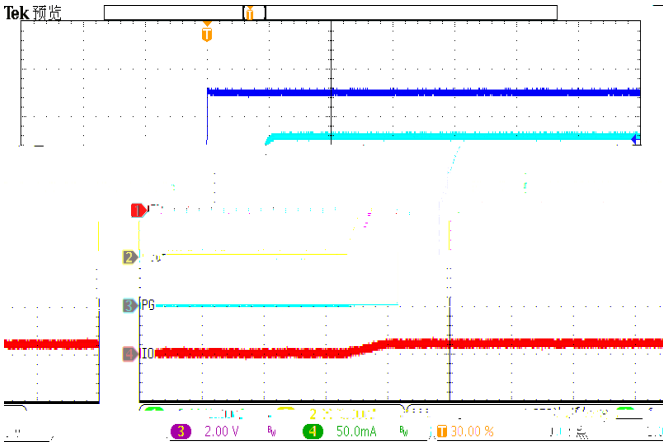
SCT71403Q Series

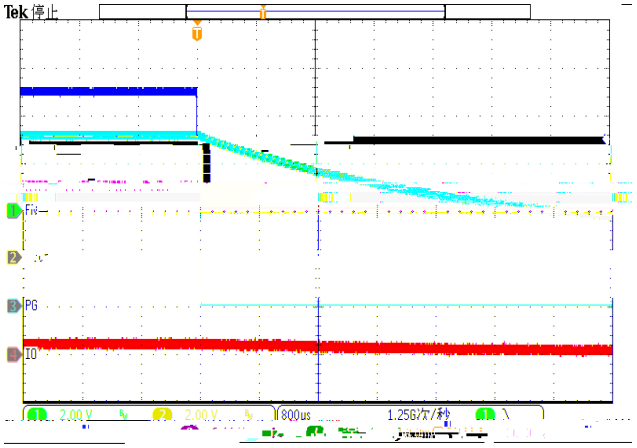
Application Waveforms





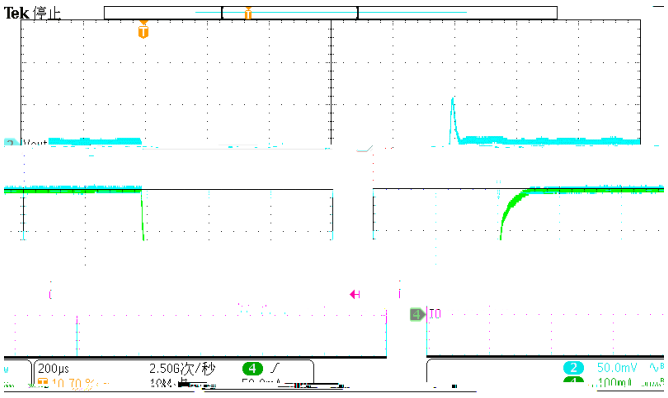


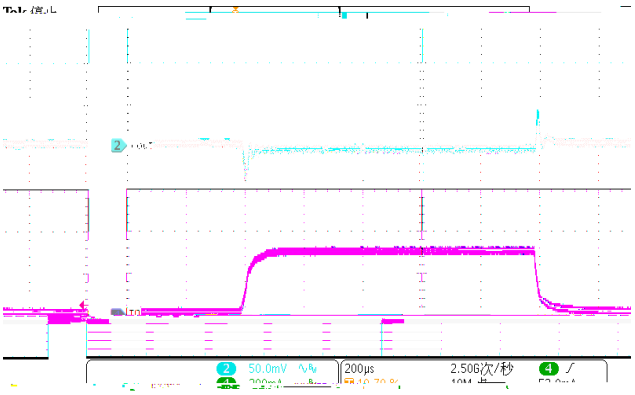


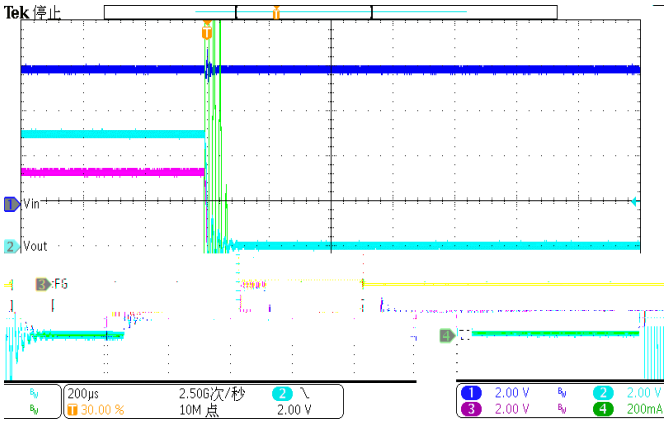


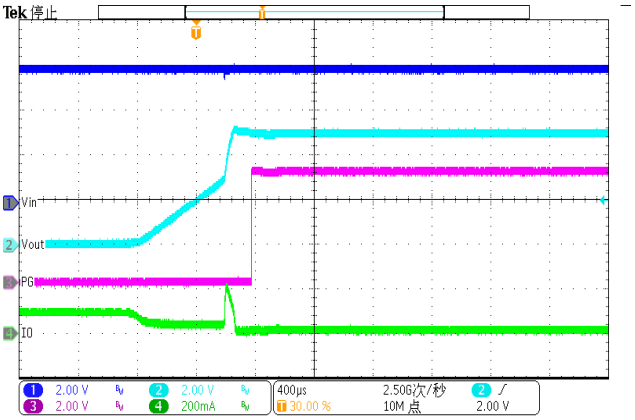
Application Waveforms(Continued)

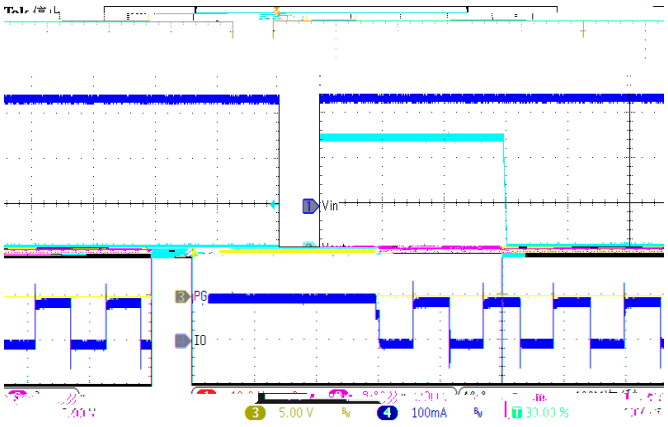
Application Waveforms(Continued)

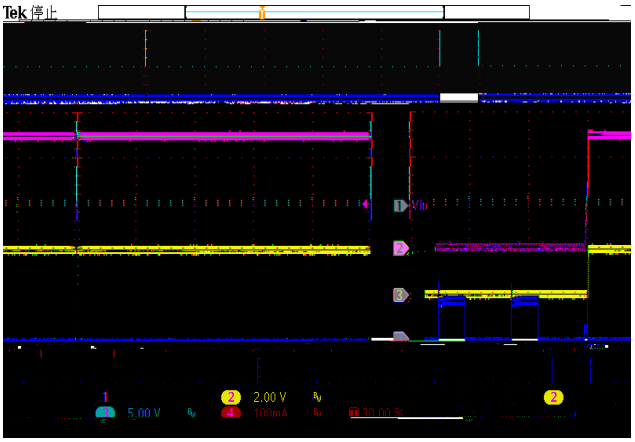










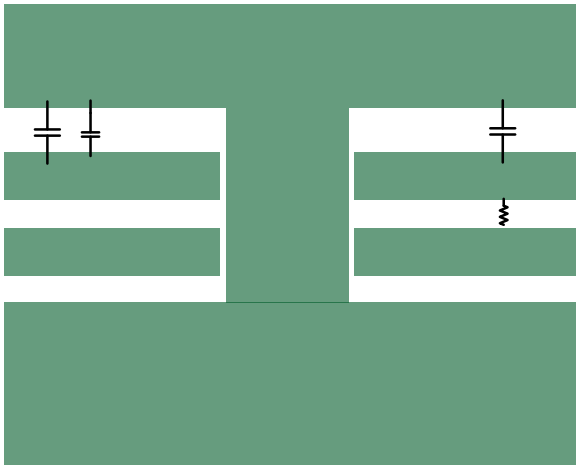


Layout Guideline

1.
2.

3.
4.

5.



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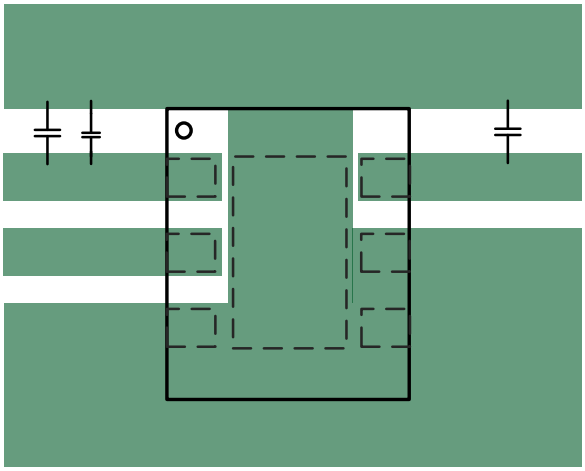

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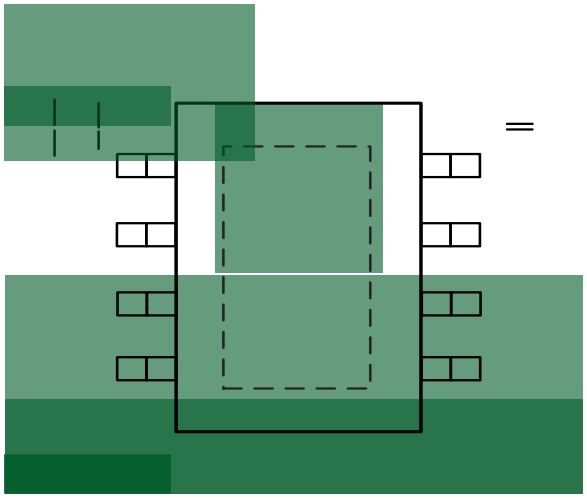

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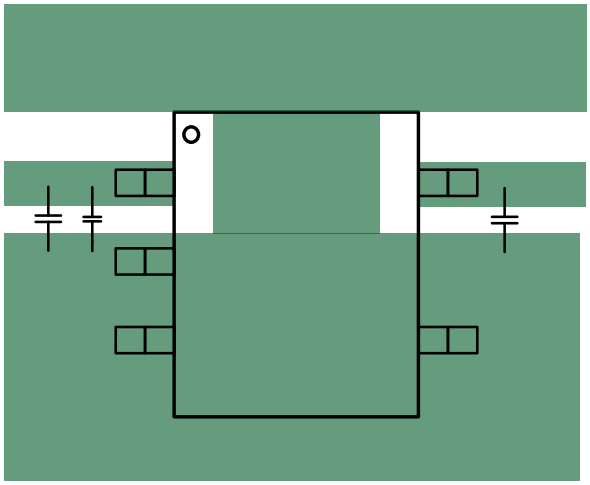

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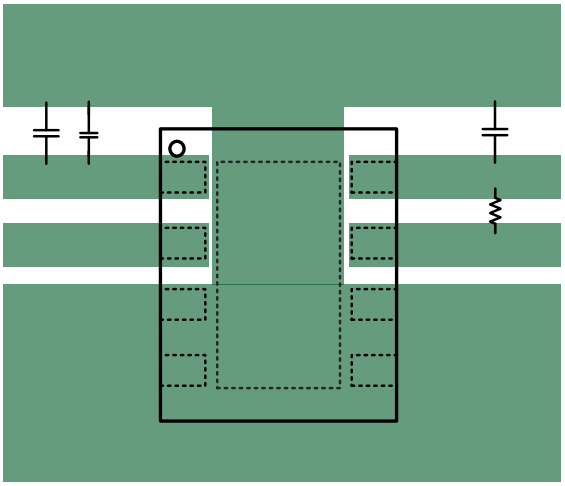

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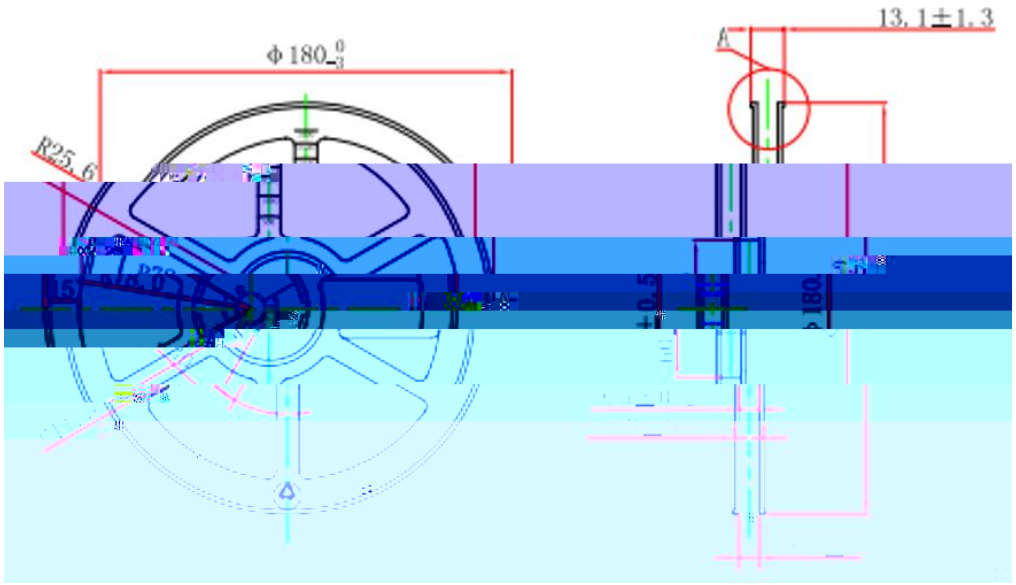

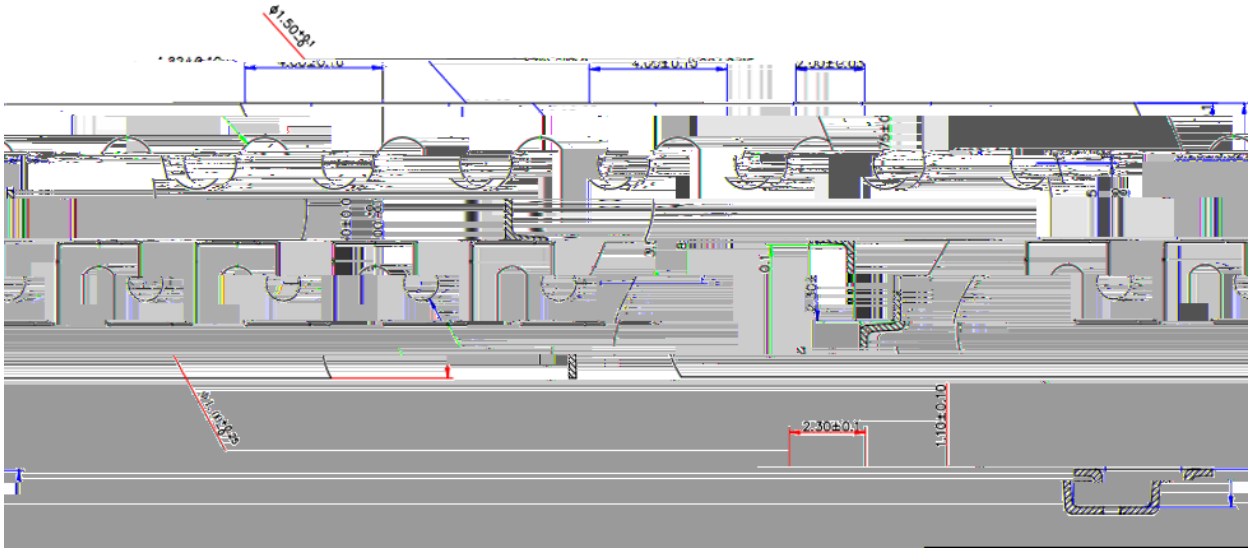

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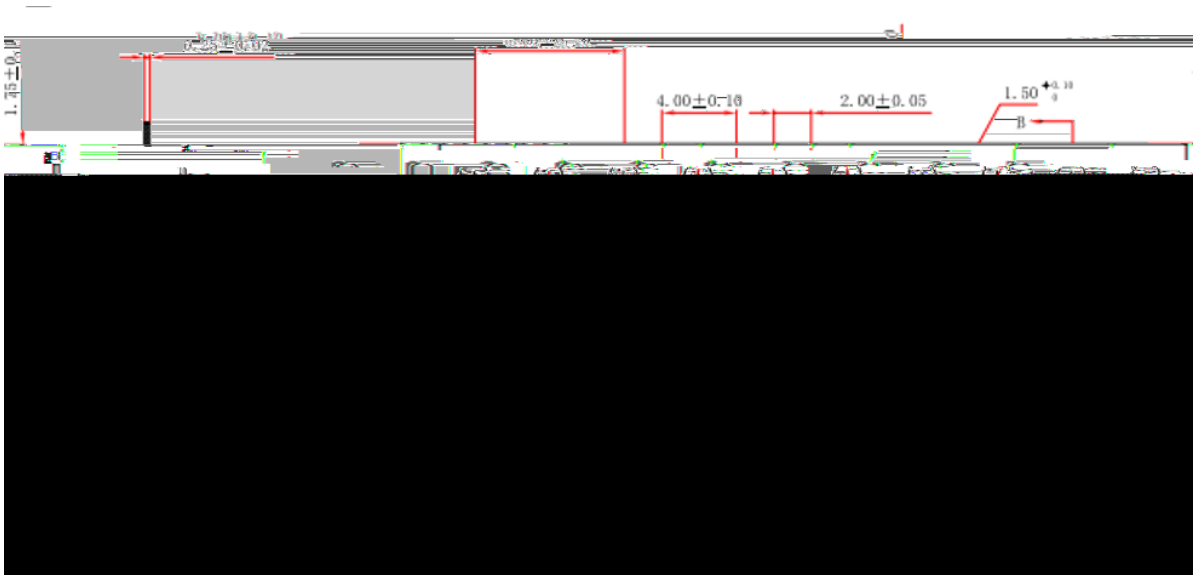

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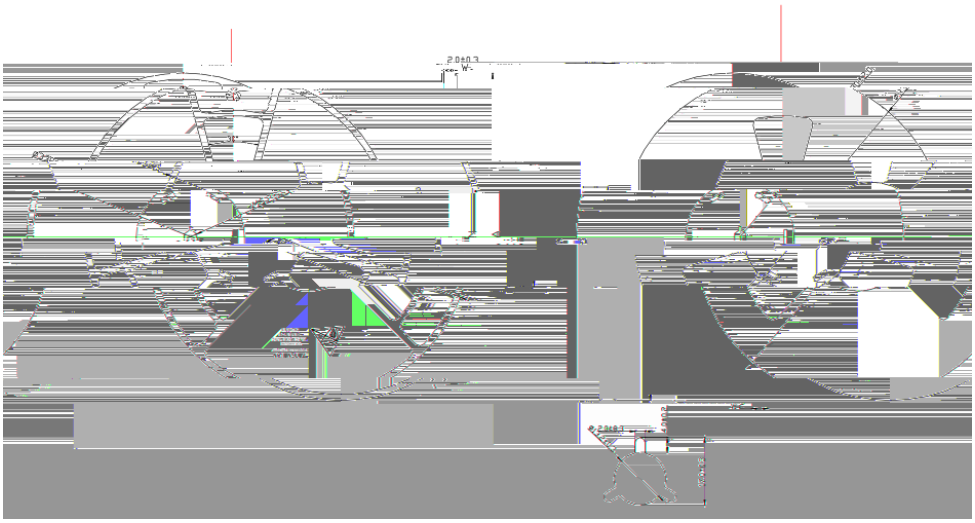

NOTICE:

SCT71403Q Series